

isc Silicon NPN RF Transistor

BFR106

DESCRIPTION

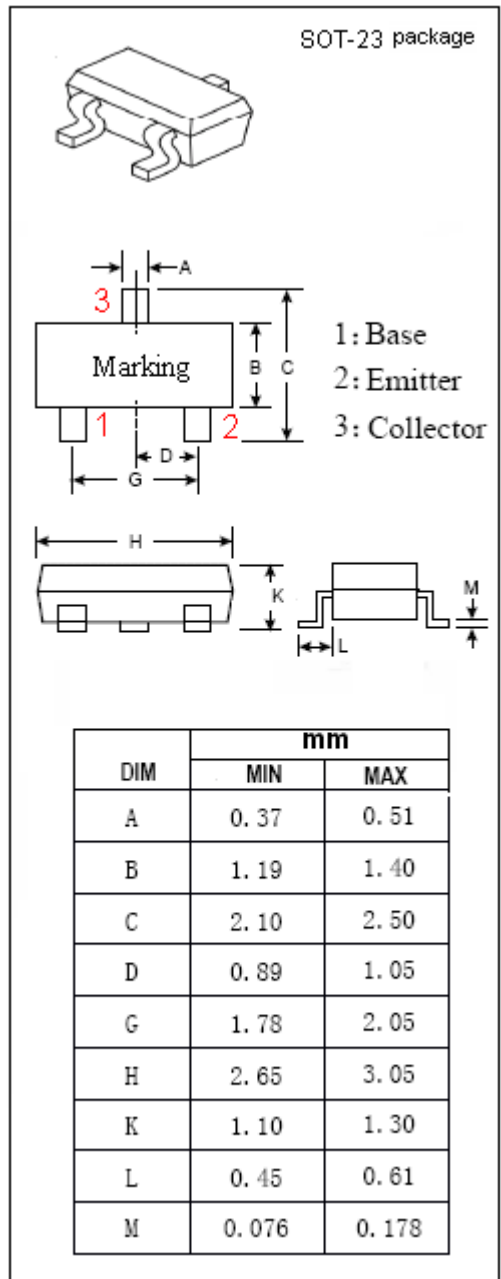
- Low Noise Figure  
 $NF = 2.5 \text{ dB TYP. @} V_{CE} = 8 \text{ V, } I_C = 20 \text{ mA, } f = 900 \text{ MHz}$
- High Gain  
 $|S_{21e}|^2 = 10.5 \text{ dB TYP. @} V_{CE} = 8 \text{ V, } I_C = 70 \text{ mA, } f = 900 \text{ MHz}$

APPLICATIONS

- Designed for use in low noise ,high-gain amplifiers and linear broadband amplifiers.

ABSOLUTE MAXIMUM RATINGS( $T_a=25^\circ\text{C}$ )

SYMBOL	PARAMETER	VALUE	UNIT
$V_{CBO}$	Collector-Base Voltage	20	V
$V_{CES}$	Collector-Emitter Voltage	20	V
$V_{CEO}$	Collector-Emitter Voltage	15	V
$V_{EBO}$	Emitter-Base Voltage	3	V
$I_C$	Collector Current-Continuous	100	mA
$I_B$	Base Current-Continuous	12	mA
$P_C$	Collector Power Dissipation @ $T_c=25^\circ\text{C}$	0.7	W
$T_J$	Junction Temperature	150	$^\circ\text{C}$
$T_{stg}$	Storage Temperature Range	-65~150	$^\circ\text{C}$



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## ELECTRICAL CHARACTERISTICS

T<sub>c</sub>=25°C unless otherwise specified

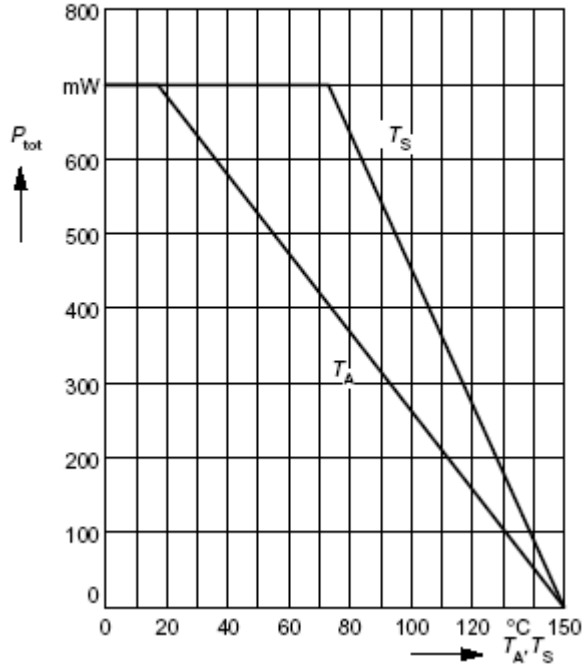
SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V <sub>(BR)CEO</sub>	Collector-Emitter Breakdown Voltage	I <sub>C</sub> = 1mA ; I <sub>B</sub> = 0	15			V
I <sub>CES</sub>	Collector Cutoff Current	V <sub>CE</sub> = 20V; V <sub>BE</sub> = 0			100	μ A
I <sub>CBO</sub>	Collector Cutoff Current	V <sub>CB</sub> = 10V; I <sub>E</sub> = 0			0.1	μ A
I <sub>EBO</sub>	Emitter Cutoff Current	V <sub>EB</sub> = 2V; I <sub>C</sub> = 0			10	μ A
h <sub>FE</sub>	DC Current Gain	I <sub>C</sub> = 70mA ; V <sub>CE</sub> = 8V	40		220	
f <sub>T</sub>	Current-Gain—Bandwidth Product	I <sub>C</sub> = 70mA ; V <sub>CE</sub> = 8V; f= 500MHz	3.5	5		GHz
C <sub>OB</sub>	Output Capacitance	I <sub>E</sub> = 0 ; V <sub>CB</sub> = 10V; f= 1MHz		0.95	1.5	pF
PG	Power Gain	I <sub>C</sub> = 70mA ; V <sub>CE</sub> = 8V; f= 900MHz		12.5		dB
PG	Power Gain	I <sub>C</sub> = 70mA ; V <sub>CE</sub> = 8V; f= 1.8GHz		7.5		dB
S <sub>21e</sub>   <sup>2</sup>	Insertion Power Gain	I <sub>C</sub> = 70mA ; V <sub>CE</sub> = 8V; f= 900MHz		10.5		dB
S <sub>21e</sub>   <sup>2</sup>	Insertion Power Gain	I <sub>C</sub> = 70mA ; V <sub>CE</sub> = 8V; f= 1.8GHz		5		dB
NF	Noise Figure	I <sub>C</sub> = 20mA ; V <sub>CE</sub> = 8V; f= 900MHz		2.5		dB
NF	Noise Figure	I <sub>C</sub> = 20mA ; V <sub>CE</sub> = 8V; f= 1.8GHz		4		dB

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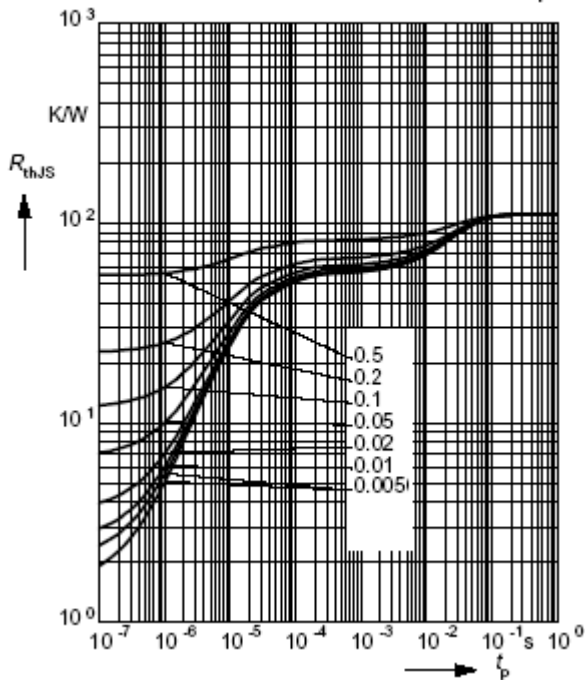
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Total power dissipation  $P_{tot} = f(T_A^*, T_S)$

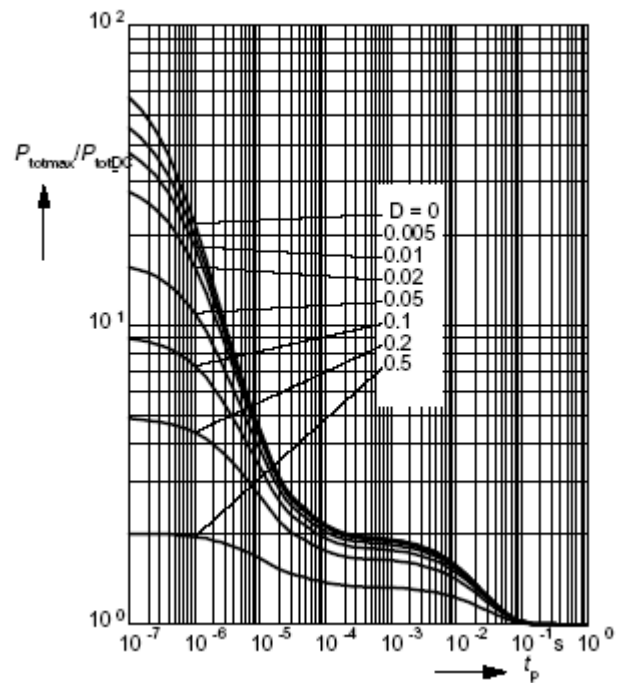
\* Package mounted on epoxy



Permissible Pulse Load  $R_{thJS} = f(t_p)$



Permissible Pulse Load  $P_{totmax}/P_{totDC} = f(t_p)$

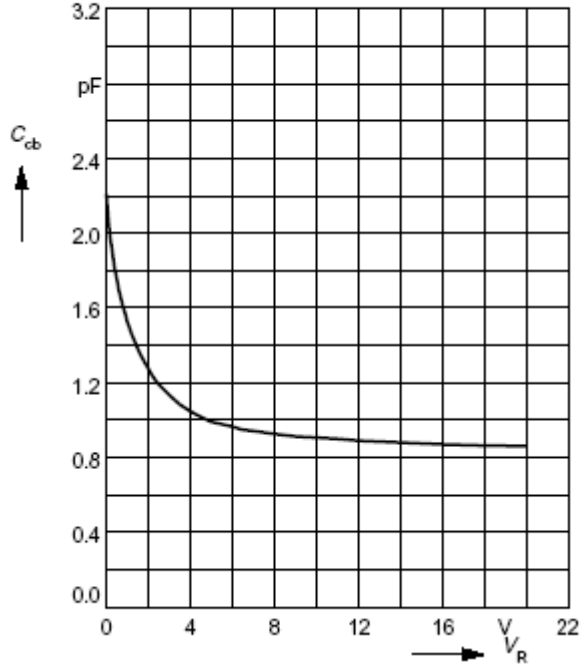


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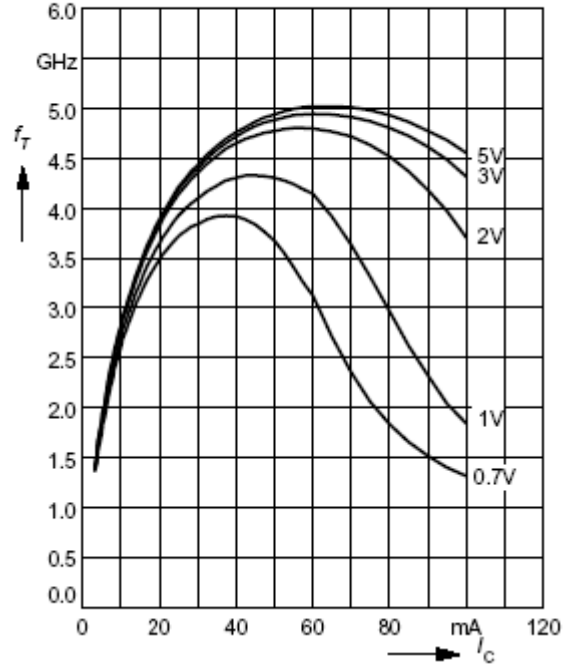
Collector-base capacitance  $C_{cb} = f(V_{CB})$

$V_{BE} = v_{be} = 0, f = 1\text{MHz}$



Transition frequency  $f_T = f(I_C)$

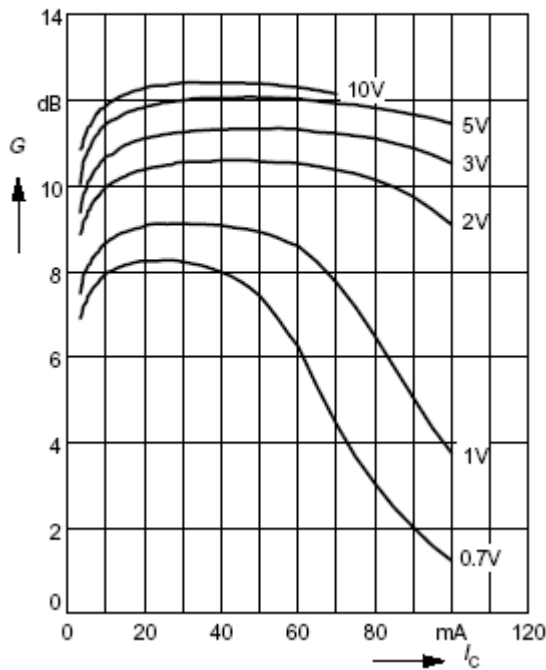
$V_{CE} = \text{Parameter}$



Power Gain  $G_{ma}, G_{ms} = f(I_C)$

$f = 0.9\text{GHz}$

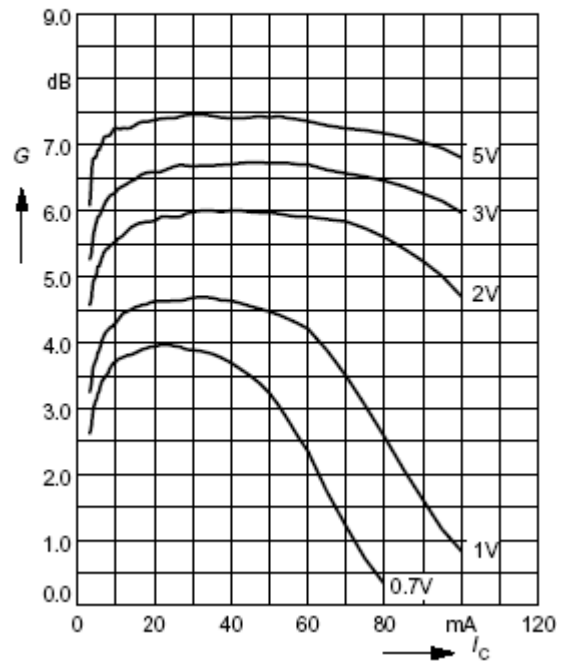
$V_{CE} = \text{Parameter}$



Power Gain  $G_{ma}, G_{ms} = f(I_C)$

$f = 1.8\text{GHz}$

$V_{CE} = \text{Parameter}$

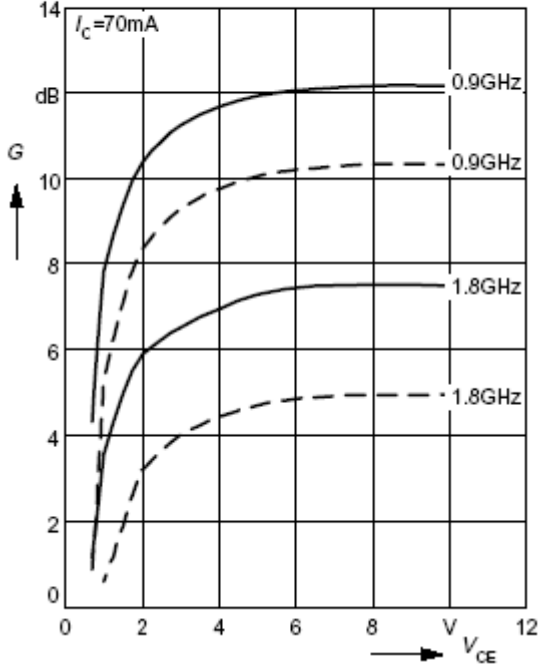


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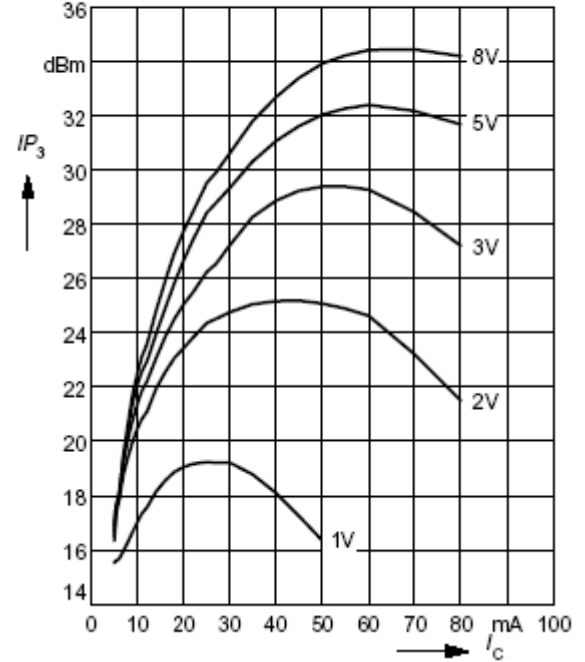
Power Gain  $G_{ma}, G_{ms} = f(V_{CE})$ : \_\_\_\_\_  
 $|S_{21}|^2 = f(V_{CE})$ : - - - - -

$f =$  Parameter

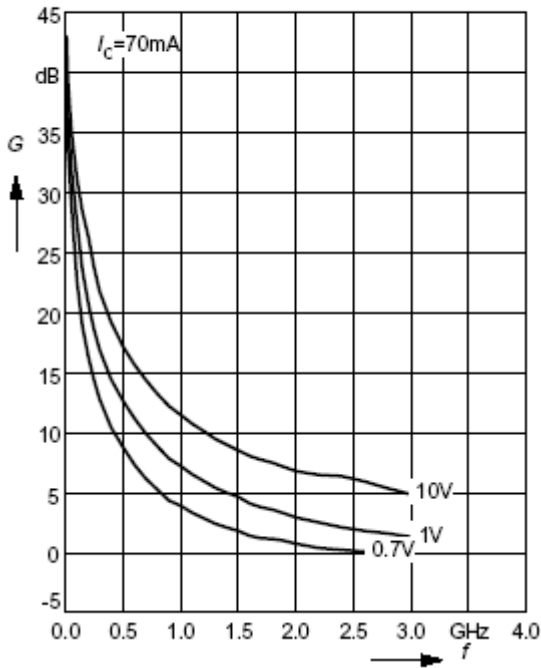


Intermodulation Intercept Point  $IP_3 = f(I_C)$   
 (3rd order, Output,  $Z_S = Z_L = 50\Omega$ )

$V_{CE} =$  Parameter,  $f = 900\text{MHz}$



Power Gain  $G_{ma}, G_{ms} = f(f)$   
 $V_{CE} =$  Parameter



Power Gain  $|S_{21}|^2 = f(f)$   
 $V_{CE} =$  Parameter

